

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,047,399 B2
APPLICATION NO. : 09/842312
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INVENTOR(S) : Andrew C. Sturges and Nathan M. Sidwell

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 3, line 28, should read:

--provided a computer system for fetching, decoding and--

Col. 4, line 32 should read:

--selected one of said instruction fetchers to said execution--

line 44, should read:

--can be done in a number of ways. For example, a further--

line 49 should read:

--register can then be compared with an instruction pointer--

Col. 6, line 8 should read as shown below:

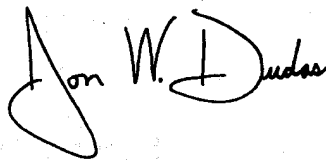
--commencing from a target location. The rejected state is one--

Col. 17, line 55 should read:

--kernel signal is asserted at the same time as the start signal--

Signed and Sealed this

Fifteenth Day of August, 2006

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with a large loop for the "J" and a cursive "Dudas".

JON W. DUDAS

Director of the United States Patent and Trademark Office